WHAT IS CLAIMED IS:

1. A driving circuit for flat panel displays disposed on a panel, comprising:

a plurality of signal lines for providing video signals;

at least one buffer unit for inverting a scanning signal; and

a plurality of switch units disposed between said plurality of signal lines;

wherein each of said plurality of switch units is connected to at least one signal line to receive a video signal and is connected to said buffer unit whereby said scanning signal controls the operation of the plurality of switch units and a video signal is outputted to an active area (display area) of said flat panel display panel.

2. The driving circuit of claim 1, wherein said plurality of switch units and said active area (display area) of said panel are spaced apart with at least one signal line.

3. The driving circuit of claim 1, wherein said buffer unit for inverting a scanning signal is an inverting circuit receiving a timing signal which is then inverted to output at least one scanning signal.

4. The driving circuit of claim 2, wherein said at least one scanning signal is an inversed signal of said timing signal.

5. The driving circuit of claim 1, wherein said plurality of switch units are thin-film transistors.

6. The driving circuit of claim 1, wherein said at least one signal line is disposed between said plurality of switch units and said buffer unit for

inverting a scanning signal.

7. The driving circuit of claim 1, wherein said plurality of signal lines are disposed between said switch units and said active area (display area).

8. The driving circuit of claim 1, wherein said panel is a liquid crystal display panel.